

FIG.1

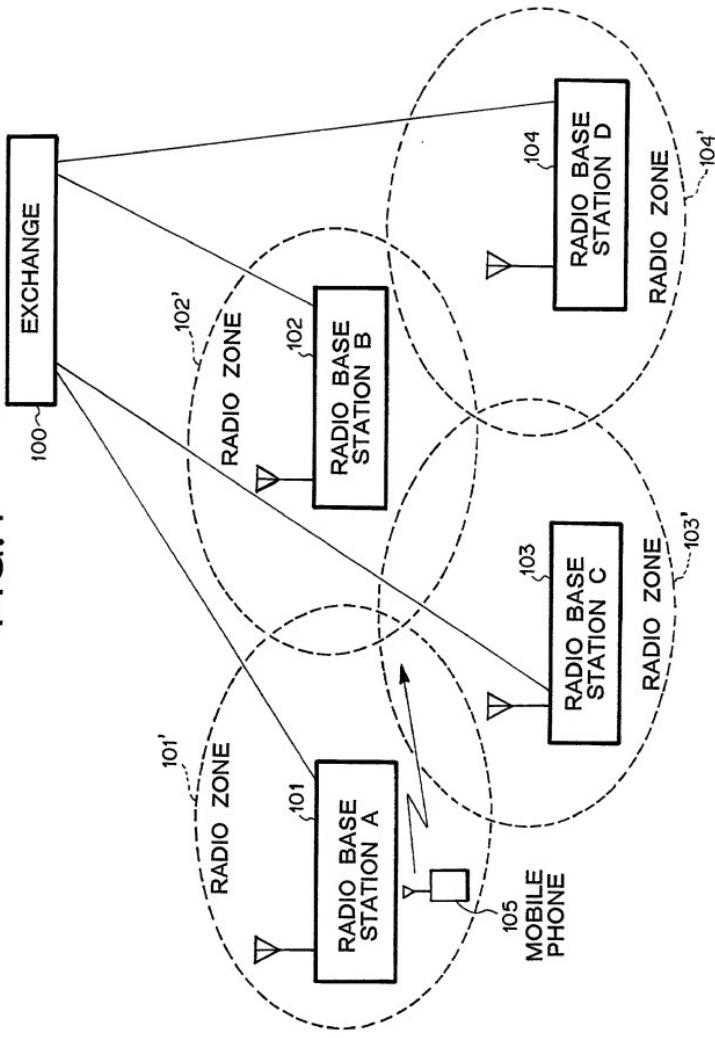


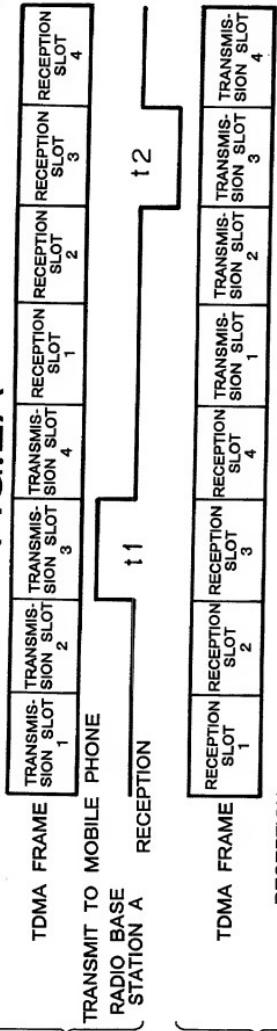
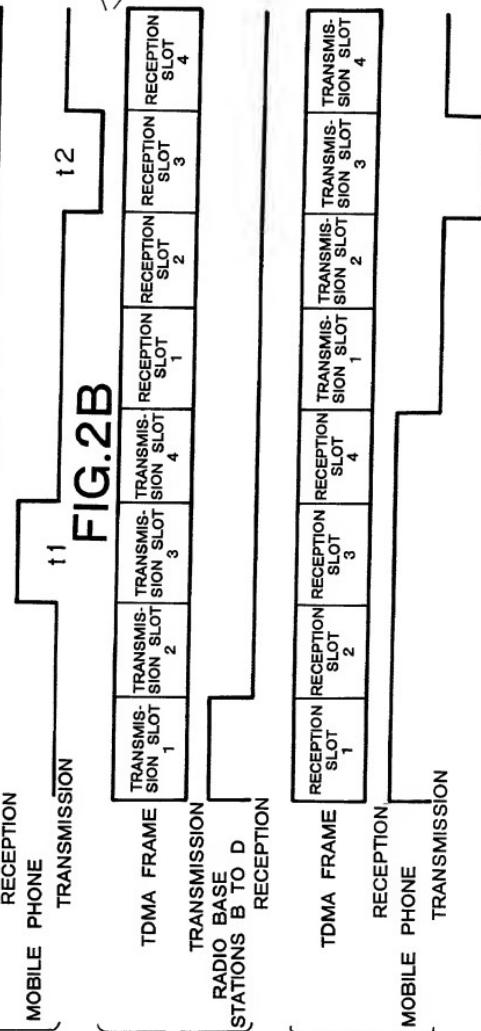
FIG. 2A**FIG. 2B**

FIG.3A

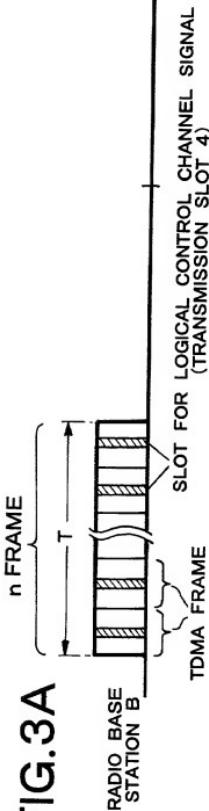


FIG.3B

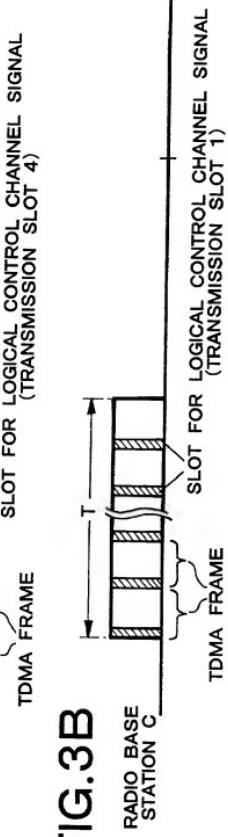


FIG.3C

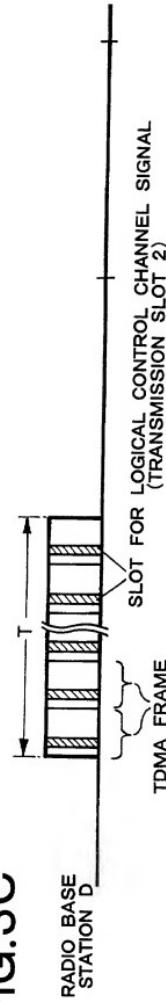


FIG. 4

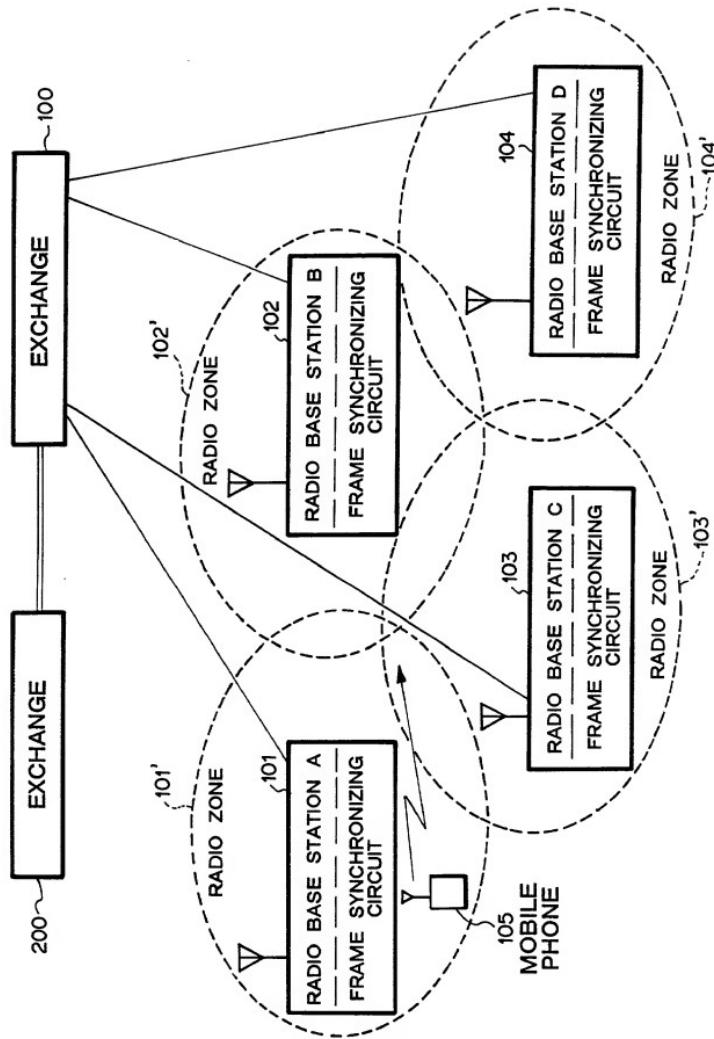


FIG.5A

TRANSMISSION SLOT 1	TRANSMISSION SLOT 2	TRANSMISSION SLOT 3	TRANSMISSION SLOT 4	RECEPTION SLOT 1	RECEPTION SLOT 2	RECEPTION SLOT 3	RECEPTION SLOT 4
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SLOT FOR LOGICAL CONTROL CHANNEL SIGNAL

FIG.5B

RECEPTION SLOT 1	RECEPTION SLOT 2	RECEPTION SLOT 3	RECEPTION SLOT 4	TRANSMISSION SLOT 1	TRANSMISSION SLOT 2	TRANSMISSION SLOT 3	TRANSMISSION SLOT 4
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FIG.6A

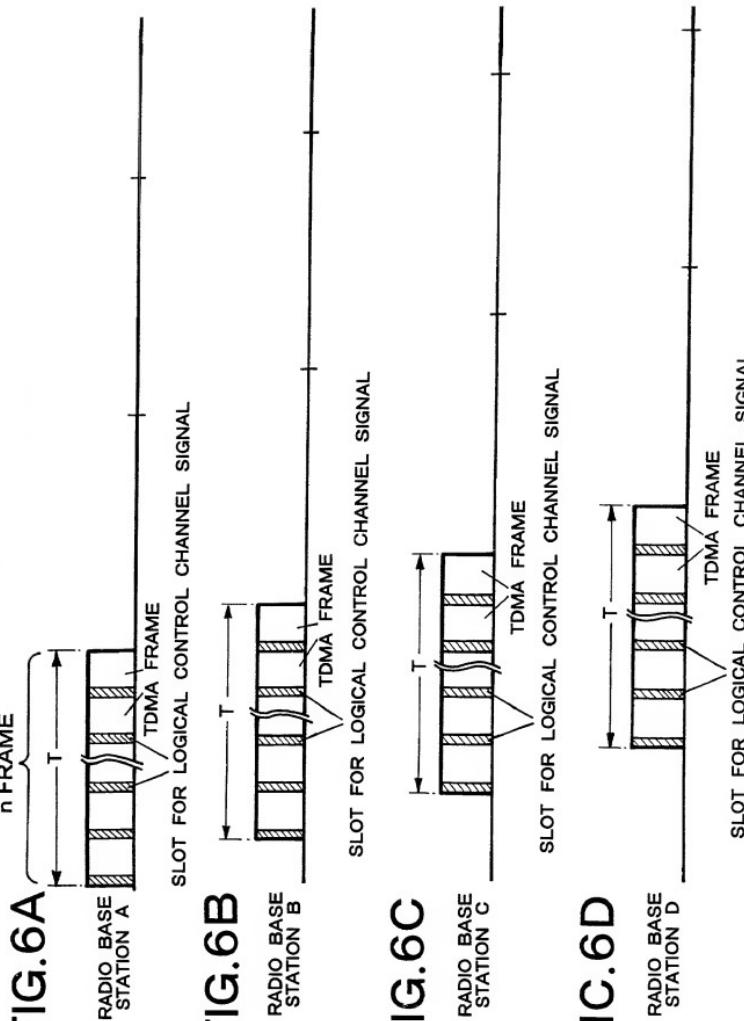


FIG.6B

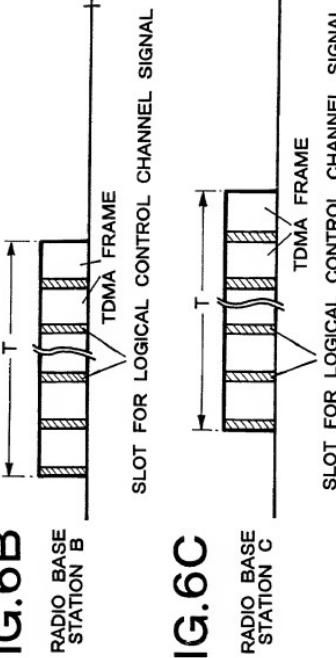


FIG.6C

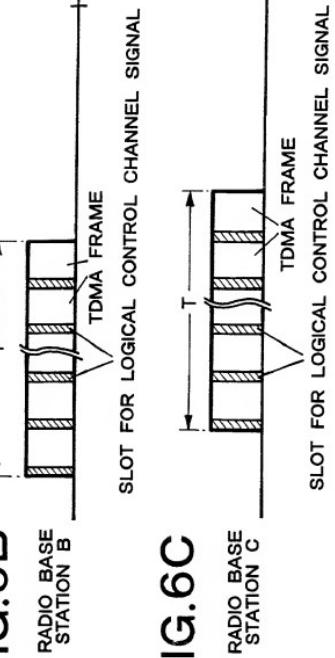


FIG.6D

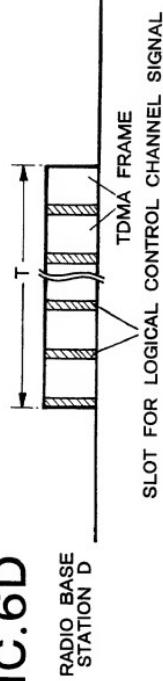
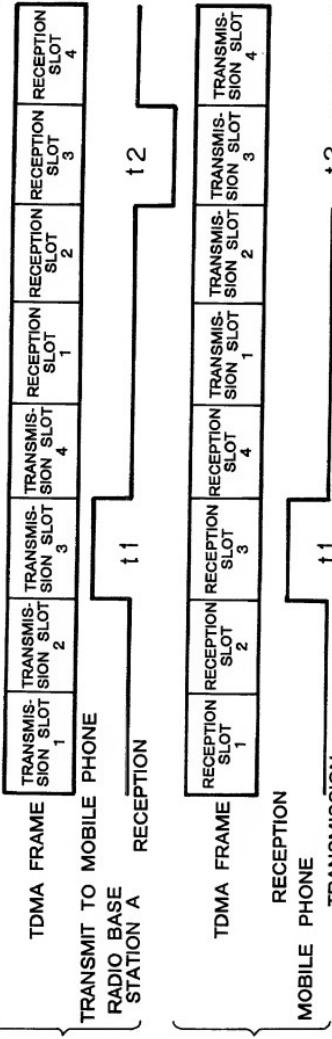


FIG. 7A**FIG. 7B**